

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 53080
	:	
Shiro SAKIYAMA et al.	:	Confirmation Number: 6689
	:	
Application No.: 10/511,165	:	Group Art Unit: 2816
	:	
Filed: October 14, 2004	:	Examiner: HILTUNEN, Thomas J.
	:	
For: SEMICONDUCTOR INTEGRATED CIRCUIT WITH REDUCED SPEED VARIATIONS		

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the non-final Office Action dated June 26, 2008, having a three-month shortened statutory period for reply set to expire on September 26, 2008, please amend the above identified application as follows:

Amendments to the Claims are reflected in the listing of claims beginning on page 2 of this paper. This listing of claims replaces all prior versions, and listings, of claims in the application.

Remarks begin on page 6 of this paper.